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Examiner Brian R. Peugh, Group Art Unit 2187

Response After Final Rejection
February 27, 2006

It is requested that the Claims be amended as follows:

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2 1. (Currently Amended) For use in a system having multiple processors in a
3 processing node coupled to a memory, a method, comprising:

4 a.) receiving multiple requests for data from the multiple processors;

5 b.) if ones of the multiple requests are requesting the same data, creating
6 a respective linked list in the processing node to record the ones of the multiple
7 requests; and

8 c.) issuing one of the requests recorded by each linked list from the
9 processing node to the memory.

1 2. (Original) The method of Claim 1, wherein each linked list orders requests in
2 order of receipt, and wherein the issued request is the oldest pending request.

1 3. (Original) The method of Claim 1, wherein the memory issues memory
2 requests to the multiple processors for the return of data to the memory, and if a
3 memory request is requesting the same data as requests recorded within a
4 linked list, adding the memory request to the linked list.

1 4. (Original) The method of Claim 1, and further including:
2 receiving requested data from the memory;
3 if the received data was requested by requests recorded in a linked list,
4 providing the received data to a processor that issued a predetermined one of
5 the requests included in the linked list;
6 removing the predetermined request from the linked list; and
7 processing all requests remaining in the linked list.

1 5. (Original) The method of Claim 4, wherein the predetermined request is the
2 oldest-pending request in the linked list.